

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	688	(702/176,177,178).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/19 20:35
S2	2	((("5845109") or ("6114880")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 11:57
S3	0	(duplicat\$3 adj logic\$3) and (monitor\$3 with ((ic \$1 with circuit\$1) with tim\$3))	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:44
S4	1	(duplicat\$3 with logic\$3) and (monitor\$3 with ((ic \$1 with circuit\$1) with tim\$3))	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:46
S5	492	(tim\$3 with closur\$3) same (integrat\$3 with circuit\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:47
S6	0	S1 and S5	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:47
S7	0	S2 and S5	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:47
S8	1	(tim\$3 with closur\$3) same (integrat\$3 with circuit\$1) and (signal\$3 with generat\$3) and (duplicat\$3 with path\$3) and (logic\$3 with circuit \$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:52

S9	1	(tim\$3 with closur\$3) same (integrat\$3 with circuit\$1) and (signal\$3 with generat\$3) and (duplicat\$3 with path\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:50
S10	1	(tim\$3 with closur\$3) same (integrat\$3 with circuit\$1) and (signal\$3 with generat\$3) and (propogat\$3 with delay\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:52
S11	14	("3959731" "3971994" "4362957" "4564837" "4691382" "4988901" "5572719" "5708375" "5834959").PN. OR ("6114880").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/19 20:54
S12	759	(714/746).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 11:15
S13	2520	hardware with tun\$3	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 11:15
S14	2	S12 and S13	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 11:15
S15	1	("20060187001").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 11:40
S16	243	(VLSI VHDL) with (time\$1 and delay\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 11:57
S17	88	(pulse with width) same (design\$3 with margin \$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:33
S18	0	((pulse with width) same (design\$3 with margin \$1)) and (circuit\$1 with tun\$3) and (clock\$3 with synchron\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:34

S19	19	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 with synchron\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:35
S20	49	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 and delay\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:37
S21	29	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 and delay\$3) and ((ris\$3 lead \$3) with edge\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:42
S22	24	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1))	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:42
S23	5	S21 and S22	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:54
S27	5	(logic\$3 near path\$1) same (switch\$3 with gate \$1) same (buffer\$1 with stage\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 15:58
S28	644	(logic\$3 with path\$1) and (switch\$3 with gate \$1) and (buffer\$1 with stage\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:01
S29	13289	(referenc\$3 with signal\$1) same ((lead\$3 with edg\$3) (ris\$3 with edge\$1))	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:45
S30	6057	(referenc\$3 with signal\$1) same ((lead\$3 with edg\$3) (ris\$3 with edge\$1)) same (clock\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:44
S31	11	S17 and S29	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 16:45

S32	542	(serial with interrupt\$3 with signal\$1) and (tim\$3 with monitor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:15
S34	5	(design\$3 with margin\$1) and (propogat\$3 with delay\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:28
S35	791	(design\$3 with margin\$1) and (propagat\$3 with delay\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:28
S36	116	(design\$3 with margin\$1) same (propagat\$3 with delay\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:29
S37	84	(design\$3 with margin\$1) same (propagat\$3 with delay\$1) and (clock\$3 with signal\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:50
S38	0	(referenc\$3 with puls\$3) same (clock with puls\$3) same (design\$3 with margin) same (delay\$3 with amount\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 17:51
S39	108	(referenc\$3 with puls\$3) (clock with puls\$3) (design\$3 with margin) (delay\$3 with amount\$1)	US-PGPUB; USPAT; USOCR	AND	ON	2007/12/20 17:51
S40	13	(referenc\$3 with puls\$3) (clock with puls\$3) (design\$3 with margin) (delay\$3 with amount\$1) (process\$3 with (unit\$1 stage\$1))	US-PGPUB; USPAT; USOCR	AND	ON	2007/12/20 17:57
S41	1	(referenc\$3 with puls\$3) (clock with puls\$3) (design\$3 with margin) (delay\$3 with amount\$1) (process\$3 with (unit\$1 stage\$1)) and (minus\$3 with half\$3)	US-PGPUB; USPAT; USOCR	AND	ON	2007/12/20 17:58

S42	4	(referenc\$3 with puls\$3) (clock with puls\$3) (design\$3 with margin) (delay\$3 with amount \$1) (process\$3 with (unit\$1 stage\$1)) and (minus\$3 differenc\$3 half\$3)	US-PGPUB; USPAT; USOCR	AND	ON	2007/12/20 18:05
S43	759	(714/746).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 18:05
S47	1	S35 and S43	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:06
S48	47	("20010016927" "20020038418" "3893070" "3905023" "4339657" "4669092" "4756005" "5043990" "5203003" "5276690" "5291496" "5313625" "5321705" "5426746" "5455536" "5504859" "5528637" "5553232" "5572662" "5615263" "5627412" "5737369" "5859551" "5870446" "5914903" "6067256" "6114880" "6173423" "6188610" "6222660" "6476643" "6523201" "6693985" "6741110" "6772388" "6799292" "6834367" "6907553" "6944468" "6958627" "6977910" "7010074" "7046056" "7085993" "7096137" "7116744" "7142623").PN. OR ("7278080"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:15
S50	2608	(702/79,80,81,89,107,117,118).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 18:19
S51	0	S48 and S50	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:19

S52	0	S50 and S17	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:19
S53	78	S50 and S29	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:19
S54	0	S50 and S29 and S42	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:19
S55	0	S53 and S22	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:19
S56	44	S53 and S30	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:20
S57	0	S56 and S32	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:20
S58	3644	(365/201).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 18:21
S59	20	S58 and S50	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:21
S60	0	S59 and S28	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:21
S61	0	S59 and S22	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:21

S62	0	S59 and S30	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:21
S63	0	S59 and S56	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:22
S64	304	(327/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/12/20 18:22
S65	12	S64 and S30	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/20 18:22
S66	3446	(702/79-81,89,107,117,118,176,177,178). CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/04 13:32
S67	3944	(365/201).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/04 13:32
S68	307	(327/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/04 13:33
S69	807	(714/746).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/04 13:33
S70	661	(logic\$3 with path\$1) and (switch\$3 with gate \$1) and (buffer\$1 with stage\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/04 13:33
S71	12	(S66 S67 S68 S69) and S70	US-PGPUB; USPAT; USOCR	OR	ON	2008/07/04 13:33

S72	4117	(702/176-178,79-81,89,107,117-120).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/20 14:26
S73	4089	(365/201).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/20 14:26
S74	312	(327/39).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/20 14:26
S75	847	(714/746).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/20 14:26
S76	21	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 with synchron\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:27
S77	0	S76 and (S72 S73 S74 S75)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:27
S78	0	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 with synchron\$3) and (monitor \$3 with circuit\$1) and (tim\$3 with closur\$3 with signal\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:28
S79	0	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 with synchron\$3) and (monitor \$3 with circuit\$1) and (tim\$3 with closur\$3 with signal\$3) and ((receiv\$3 receip\$3 timestamp\$3) with (clock\$1 with signal\$1))	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:28

S80	0	((pulse with width) same (design\$3 with margin \$1)) and (clock\$3 with synchron\$3) and (monitor \$3 with circuit\$1) and (tim\$3 with clousr\$3 with signal\$3) and ((receiv\$3 receip\$3 timestamp\$3) with (clock\$1 with signal\$1)) and (duplicat\$3 with logic\$3 with path\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:28
S81	0	S80 and (S72 S73 S74 S75)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:29
S82	9	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:29
S83	1	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:30
S84	1	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:30
S85	1	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1) and (signal\$3 with generat\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:30

S86	0	((referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1) and (signal\$3 with generat\$4)).clm.	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:30
S87	0	S85 and (S72 S73 S74 S75)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:31
S88	0	S86 and (S72 S73 S74 S75)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/20 14:31
S89	2260	(326/93,95,98).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/24 16:10
S90	1	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1) and (signal\$3 with generat\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:10
S91	0	((clock\$3 tim\$3) with (signal\$3 and circuit\$1)) same (compar\$3) same (reference\$1 with signal \$1) same (duplicat\$3 with logic\$3 with path\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:11
S92	0	S89 and S90	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:11

S93	0	((clock\$3 tim\$3) with (signal\$3 and circuit\$1)) same (compar\$3) same (reference\$1 with signal \$1) same (duplicat\$3 with logic\$3 with path\$1)). clm.	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:11
S94	0	S91 and S89	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:11
S95	0	S92 and S89	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:11
S96	1706	(327/141).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/11/24 16:42
S97	0	((clock\$3 tim\$3) with (signal\$3 and circuit\$1)) same (compar\$3) same (reference\$1 with signal \$1) same (duplicat\$3 with logic\$3 with path\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:46
S98	0	S96 and S97	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:46
S99	1	(referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv \$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:46
S101	0	S96 and S99	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:47

S103	0	((referenc\$3 with signal\$1) same (puls\$3 with width\$1) same (margin\$1 with (value\$1 threshold\$1 design\$1)) and ((receipt\$1 receiv\$3 timestamp\$3) with signal with clock\$1) and (logic with path\$1) and (design with margin\$1) and (tim\$3 with characteristic\$1)).clm.	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:48
S104	0	S96 and S103	US-PGPUB; USPAT; USOCR	OR	ON	2008/11/24 16:48

11/24/2008 4:58:10 PM

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